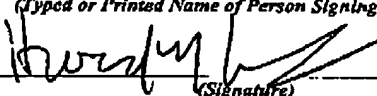


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CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)			Docket No.
Applicant(s): Belyansky, et al.			FIS920040047US1
Application No. 10/709,652	Filing Date May 20, 2004	Examiner Walter Lee Lindsay Jr.	Group Art Unit 2812
Invention: REDUCED DIELECTRIC CONSTANT SPACER MATERIALS INTERGRATION FOR HIGH SPEED LOGIC GATES			
<p>I hereby certify that this <u>Amendment After Final</u> (Identify type of correspondence)</p> <p>is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. <u>571-273-8300</u>)</p> <p>on <u>January 4, 2006</u> (Date)</p> <p style="text-align: right;"><u>Howard M. Cohn</u> (Typed or Printed Name of Person Signing Certificate)</p> <p style="text-align: right;"> (Signature)</p> <p style="text-align: center;">Note: Each paper must have its own certificate of mailing.</p>			

P18/REV02

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Appl. No. 10/709,652
Amdt. Dated 01/04/2006
Reply to Office action of November 29, 2005

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Confirmation No. 3651

In re : Application of Belyansky, et al.
For : REDUCED DIELECTRIC CONSTANT SPACER MATERIALS
INTEGRATION FOR HIGH SPEED LOGIC GATES
Serial No. : 10/709,652
Filed : May 20, 2004
Group Art Unit : 2812
Examiner : Walter Lee Lindsay, Jr.
Our Docket No. : FIS920040047US1
Customer No. : 24036

January 4, 2006

By Fax 571-273-8300

COMMISSIONER FOR PATENTS

P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

This is in response to the Final Office Action dated November 29, 2005 having a shortened statutory period of response set to expire on February 28, 2006.

Please amend the referenced application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.